

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Daniel Yellin et al.                      Art Unit: 2133  
Serial No.: 09/880,707                                  Examiner: Joseph D. Torres  
Filed : June 12, 2001  
Assignee : Intel Corporation  
Title : LOW COMPLEXITY CHANNEL DECODERS

**MAIL STOP RCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made with a Request for Continued Examination (RCE). No fee is required.

Desig. ID "AA" is the publication for a related application, U.S. patent application no. 10/829,075 (hereafter referred to as '075). Desig. IDs "AB-AG", "AN", "AP-AQ" and "AS" were cited by the Examiner in a Notice of Allowance mailed on September 21, 2006 in the related application '075. Desig. IDs "AO" and "AR" are mentioned in the section of the specification entitled "BACKGROUND", pages 2-3.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

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Attorney's Docket No. 10559-449001  
P10766

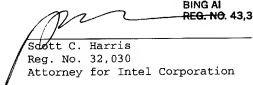
Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any credits or additional charges to deposit account 06-1050.

Respectfully submitted,

/BY  
BING AI  
REG. NO. 43,312

Date: April 13, 2007



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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-449001	Application No. 09/880,707
		Applicant Daniel Yellin et al.	
		Filing Date June 12, 2001	Group Art Unit 2133

**Information Disclosure Statement**  
by Applicant  
(Use several sheets if necessary)

(37 CFR §1.98(b))

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2004/0199856	10/2004	Yellin et al.			
	AB	4,558,350	12/1985	Murakami			
	AC	5,191,548	03/1993	Balkanski et al.			
	AD	5,297,170	03/1994	Eyuboglu et al.			
	AE	5,319,707	06/1994	Wasilewski et al.			
	AF	5,563,660	10/1996	Tsukagoshi			
	AG	5,974,181	10/1999	Prieto			
	AH						
	AI						
	AJ						

**Foreign Patent Documents or Published Foreign Patent Applications**

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AK							
	AL							
	AM							

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
	AN	Açikeli, Ö., "Implementation Issues for High Rate Turbo Codes on BPSK/QPSK Channels", <i>IEEE Global Telecommunications Conference - Globecom '99</i> , vol. 1A, pp. 148-152, (1999).
	AO	Bahl, L.R., et al., "Optimal Decoding of Linear Codes for Minimizing Symbol Error Rate", <i>IEEE Trans. Inform. Theory</i> , pp. 248-287, March 1974.
	AP	Bayazit, U., et al., "Improving the Performance of Optimal Joint Decoding", <i>IEEE Proc. International Conference on Image Processing</i> , vol. 1, pp. 113-116, (1995).
	AQ	Fischer, T., et al., "Trellis-coded Vector Quantization", <i>IEEE Transactions on Information Theory</i> , 37(6):1551-1566, November 1991.
	AR	Forney, G.D., "The Viterbi Algorithm", <i>Proc. IEEE</i> , 61(3):268-278, March 1973.
	AS	Park, G., et al., "A Block-wise MAP Decoder Using a Probability Ratio for Branch Metrics", <i>IEEE VTC '99</i> , vol. 3, pp. 1610-1614, (1999).
	AT	

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.